

1004 Nov
After
With

Amendments to the Claims

Please amend the claims as shown below.

1. (Currently amended) A configurable interface circuit comprising:
a first internal circuit operable to provide a first internal signal via a first internal signal path;
an input buffer operable to receive a first external signal via a first external signal path;
a selector circuit coupled to the first internal circuit via the first internal signal path, and to the input buffer, the selector circuit operable to select either the first internal signal or the first external signal to provide a selected signal;
an output buffer operative to receive a second internal signal via a second internal signal path and to provide the second internal signal via the first external signal path; and
a second internal circuit operable to provide the second internal signal via the second internal signal path and to receive the selected signal via a third internal signal path, the selector circuit inoperable to receive the second internal signal.
2. (Canceled)
3. (Original) The configurable interface circuit of Claim 1, wherein:
the first internal signal path and the first external signal path are operable to propagate signals in accordance with a common protocol.
4. (Original) The configurable interface circuit of Claim 3, wherein:
protocol is a PCI bus protocol.
5. (Original) The configurable interface circuit of Claim 3, wherein:
protocol is an AGP bus protocol.
6. (Original) The configurable interface circuit of Claim 3, wherein:
protocol is an NGP bus protocol.